**VIET NAM NATIONAL UNIVERSITY HO CHI MINH CITY**

**UNIVERSITY OF TECHNOLOGY FACULTY OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**DEPARTMENT OF ELECTRONICS**

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**Capstone Project 1**

**RISC-V Processor with branch comparator**

**another model of ADDER**

**HK242**

**Date: ………………**

**Lecturer: Ths…..**

|  |  |
| --- | --- |
| **Full Name** | **Student’s ID** |
| Đào Nguyên Khôi | 2151215 |

**Hồ Chí Minh City – 2025**

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# fg**Introduction**

**Chapter 1: Introduction to topic**

* 1. **Reason for selecting the topic**
  2. **Reasearch objective.**
  3. **Implementation content**
  4. **Subjects and scope of research**

**Chapter 2: Theoretical basis**